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Question Paper Code: 86570

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2021.

Fifth Semester

Electronics and Communication Engineering

EC 1302 — DIGITAL SIGNAL PROCESSING

(Regulations 2008)

Time: Three hours Maximum: 100 marks

Answer ALL questions.

PART A — $(10 \times 2 = 20 \text{ marks})$

- 1. Distinguish between DFT and DTFT.
- 2. Write any two properties of DFT.
- 3. Write the Hamming window equation used for design of FIR filters.
- 4. Mention two advantages of IIR filters.
- 5. What is the possible range of numbers in the fixed point-arithmetic?
- 6. What is meant by quantization of analog signals?
- 7. Write the underlying principle of Power Spectral Density estimation.
- 8. Give the features of Tukey method of PSD estimation.
- 9. Can embedded general-purpose RISC processors be used as DSP? Justify.
- 10. Describe about Harvard architecture.

PART B — $(5 \times 16 = 80 \text{ marks})$

11. (a) (i) Determine the 6-point DFT of the signal $x(n) = \{3, 2, 1, 0, 1, 2\}. \tag{10}$

(ii) Represent DFT and IDFT in matrix form. (6)

Or

(b) Develop 8-point radix-2 decimation in time algorithm with input in normal order and output in digit reversed order. Derive the necessary equations and show the flow diagrams.

